

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: Unassigned
Examiner: Unassigned

~~RAV~~
In Re PATENT APPLICATION Of:

Applicant(s): Hiroomi TSUTAE)
Serial No.: 10/657,207) **SUBMISSION OF**
Filed: September 9, 2003) **FORMAL DRAWINGS**
For: Process of cleaning a semiconductor)
manufacturing system and method of)
manufacturing a semiconductor device)
Attorney Ref: FUJI 136)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby submits two sheets of replacement formal drawings, FIGS. 1 and 2. The formal drawings are being submitted simultaneously with a Notice Under 37 CFR 1.251 – Pending Application, together with the Rabin & Berdo firm's PTO documents for the above-referenced PTO Lost Application File.

Respectfully submitted,

October 31, 2005
Date

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SMR:pjl

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FIG. 1

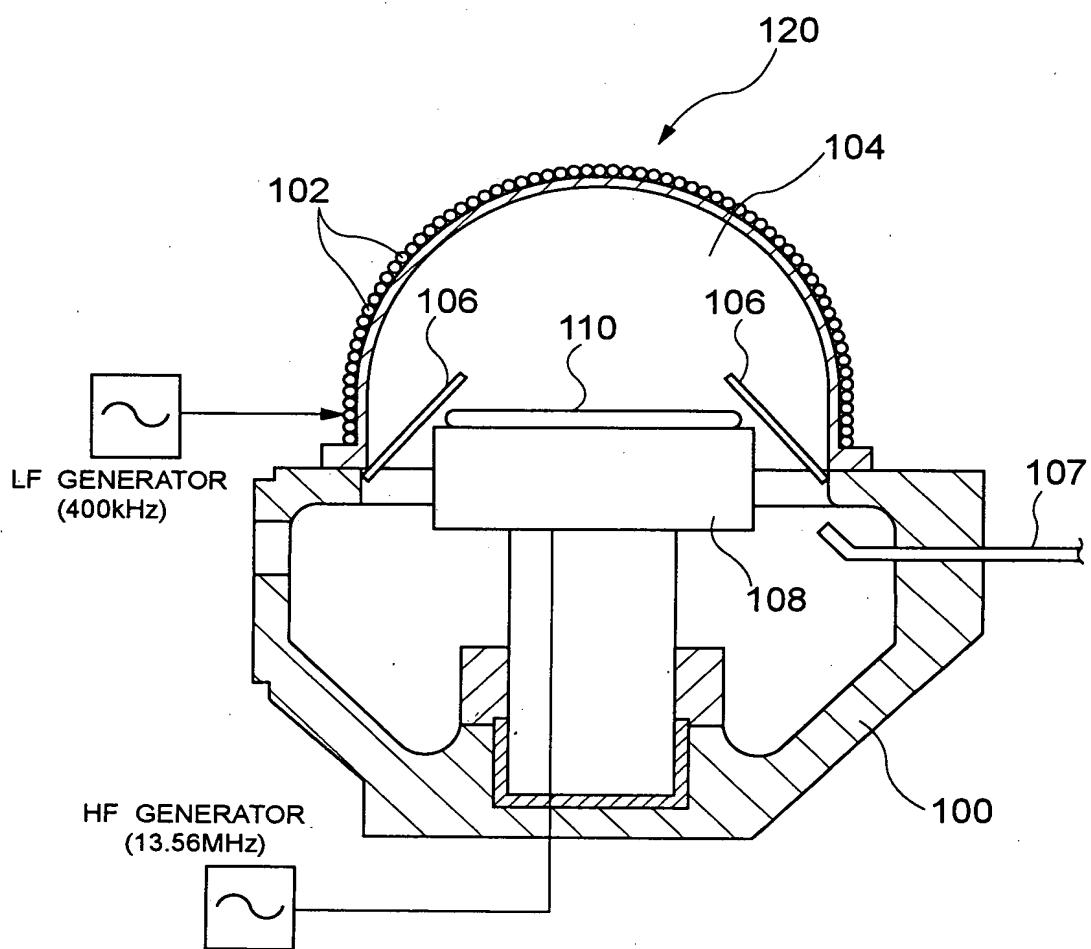


FIG. 2

